

**Notice of References Cited**

Application/Control No.

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Applicant(s)/Patent Under  
Reexamination  
KIMURA, HAJIME

Examiner

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Art Unit

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,229,506 B1	05-2001	Dawson et al.	345/82
	B	US-5,576,857 A	11-1996	Takemura, Yasuhiko	349/42
	C	US-6,628,363 B1	09-2003	Sano et al.	349/151
	D	US-5,888,858 A	03-1999	Yamazaki et al.	438/162
	E	US-6,535,185 B2	03-2003	Kim et al.	345/76
	F	US-6,091,203 A	07-2000	Kawashima et al.	315/169.3
	G	US-6,570,338 B2	05-2003	Bae, Sung Joon	315/169.3
	H	US-5,818,068 A	10-1998	Sasaki et al.	257/59
	I	US-6,011,532 A	01-2000	Yanai et al.	345/92
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Sedra and Smith, Microelectronic Circuits, 3rd ed. (New York; Saunders College Publishing, 1991) pp. 462 – 466
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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